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this practical tool independent guide to designing digital circuits takes a unique top down approach reflecting the nature of the design process in industry starting with architecture design the book comprehensively explains the why and how of digital circuit design using the physics designers need to know and no more

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practical low power digital vlsi design emphasizes the optimization and trade off techniques that involve power dissipation in the hope that the readers are better prepared the next time they are presented with a low power design problem the book highlights the basic principles methodologies and techniques that are common to most cmos digital designs the advantages and disadvantages of a particular low power technique are discussed besides the classical area performance trade off the impact to design cycle time complexity risk testability and reusability are discussed the wide impacts to all aspects of design are what make low power problems challenging and

interesting heavy emphasis is given to top down structured design style with occasional coverage in the semicustom design methodology the examples and design techniques cited have been known to be applied to production scale designs or laboratory settings the goal of practical low power digital vlsi design is to permit the readers to practice the low power techniques using current generation design style and process technology practical low power digital vlsi design considers a wide range of design abstraction levels spanning circuit logic architecture and system substantial basic knowledge is provided for qualitative and quantitative analysis at the different design abstraction levels low power techniques are presented at the circuit logic architecture and system levels special techniques that are specific to some key areas of digital chip design are discussed as well as some of the low power techniques that are just appearing on the horizon practical low power digital vlsi design will be of benefit to vlsi design engineers and students who have a fundamental knowledge of cmos digital design

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low power and low energy vlsi has become an important issue in today s consumer electronics this book is a collection of pioneering applied research papers in low power vlsi design and technology a comprehensive introductory chapter presents the current status of the industry and academic research in the area of low power vlsi design and technology other topics cover logic synthesis floorplanning circuit design and analysis from the perspective of low power requirements the readers will have a sampling of some key problems in this area as the low power solutions span the entire spectrum of the design process the book also provides excellent references on up to date research and development issues with practical solution techniques

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