

Practical Low Power Digital Vlsi Design By Gary K Yeap Download

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power consumption has become a major design consideration for battery operated portable systems as well as high performance

desktop systems strict limitations on power dissipation must be met by the designer while still meeting ever higher computational requirements a comprehensive approach is thus required at all levels of system design ranging from algorithms and architectures to the logic styles and the underlying technology potentially one of the most important techniques involves combining architecture optimization with voltage scaling allowing a trade off between silicon area and low power operation architectural optimization enables supply voltages of the order of 1 v using standard cmos technology several techniques can also be used to minimize the switched capacitance including representation optimizing signal correlations minimizing spurious transitions optimizing sequencing of operations activity driven power down etc the high efficiency of dc dc converter circuitry required for efficient low voltage and low current level operation is described by stratakos sullivan and sanders the application of various low power techniques to a chip set for multimedia applications shows that orders of magnitude reduction in power consumption is possible the book also features an analysis by professor meindl of the fundamental limits of power consumption achievable at all levels of the design hierarchy svensson of isi describes emerging adiabatic switching techniques that can break the cv2f barrier and reduce the energy per computation at a fixed voltage srivastava of at t presents the application of aggressive shut down techniques to microprocessor applications

practical low power digital vlsi design emphasizes the optimization and trade off techniques that involve power dissipation in the hope that the readers are better prepared the next time they are presented with a low power design problem the book highlights the basic principles methodologies and techniques that are common to most cmos digital designs the advantages and disadvantages of a particular low power technique are discussed besides the classical area performance trade off the impact to design cycle time complexity risk testability and reusability are discussed the wide impacts to all aspects of design are what make low power problems challenging and interesting heavy emphasis is given to top down structured design style with occasional coverage in the semicustom design methodology the examples and design techniques cited have been known to be applied to production scale designs or laboratory settings the goal of practical low power digital vlsi design is to permit the readers to practice the low power techniques using current generation design style and process technology practical low power digital vlsi design considers a wide range of design

abstraction levels spanning circuit logic architecture and system substantial basic knowledge is provided for qualitative and quantitative analysis at the different design abstraction levels low power techniques are presented at the circuit logic architecture and system levels special techniques that are specific to some key areas of digital chip design are discussed as well as some of the low power techniques that are just appearing on the horizon practical low power digital vlsi design will be of benefit to vlsi design engineers and students who have a fundamental knowledge of cmos digital design

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interest in indoor wireless communications has been increasing in addition to high throughput wlan systems such as 802 11a b g n attention is also being focused on lower rate short distance systems such as bluetooth and zigbee these low rate radios are being

proposed for a variety of applications including automation security smart toys remote sensing control asset tracking and as a replacement for computer peripheral wires while not demanding aggressive throughput these radios do require low cost power efficient operation and optionally the ability to perform ranging unfortunately currently reported radios are up to an order of magnitude away from these power and cost targets or do not support ranging however a recent ruling from the fcc has opened up nearly 8ghz of unlicensed spectrum from dc to 960mhz and from 3 1ghz to 10 6ghz for ultra wideband uwb deployment one attractive method of uwb signaling that seems suited to a low power highly integrated implementation communicates with short pulses on the order of a nanosecond that spread energy over at least 500mhz of bandwidth termed impulse uwb the baseband nature of this signaling promises low cost and low power consumption through design simplicity pulsed or duty cycled operation and a mostly digital implementation the benefits of this approach are balanced by the risk of jamming from in band interference of stricter sampling and gain constraints and of increased digital complexity this dissertation presents the system exploration specification design and demonstration of a low power highly integrated flexible baseband impulse ultra wideband transceiver front end

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this richly illustrated project book provides fast track training for hobbyists and technicians wishing to build and design digital video equipment using an easy to learn building block approach that starts with function blocks then converts them to schematics and finally design flowcharts the book allows even novices to build complex video devices 12 complete projects are included and can be customized for use in the reader s own designs

a textbook for courses in digital electronics and microprocessors offered in departments of electrical engineering technology or computer science the book covers the basics of digital logic design and the design of microprocessor based systems also covered are computer fundamentals and microprocessor hardware and software 8085 with many programming examples the text describes most important available microprocessors with laboratory exercises instructional objectives and self evaluation questions

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